

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic circuit, comprising:  
  
a first circuit unit through which a first current having a first current level passes;  
  
a capacitor element to store a quantity of electric charge corresponding to the first current level; and  
  
a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,  
  
at least one of the first circuit unit and the second circuit unit including a plurality of transistors connected in series or in parallel,  
  
respective gates of the transistors being mutually connected,  
  
the first circuit unit and the second circuit unit constituting a current mirror circuit.
2. (Currently Amended) An electronic circuit, comprising:  
  
a first circuit unit through which a first current having a first current level passes;  
  
a capacitor element to store a quantity of electric charge corresponding to the first current level; and  
  
a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,  
  
the first circuit unit including a plurality of transistors connected in parallel,

respective gates of the transistors being mutually connected,  
the first circuit unit and the second circuit unit constituting a current mirror  
circuit.

3. (Currently Amended) An electronic circuit, comprising:  
a first circuit unit through which a first current having a first current level  
passes;  
a capacitor element to store a quantity of electric charge corresponding to the  
first current level; and  
a second circuit unit to generate a second current having a second current level  
different from the first current level on the basis of the quantity of electric charge stored in the  
capacitor element,  
the second circuit unit including a plurality of transistors connected in series,  
respective gates of the transistors being mutually connected,  
the first circuit unit and the second circuit unit constituting a current mirror  
circuit.

4. (Currently Amended) An electronic circuit, comprising:  
a first circuit unit through which a first current having a first current level  
passes;  
a capacitor element to store a quantity of electric charge corresponding to the  
first current level; and  
a second circuit unit to generate a second current having a second current level  
different from the first current level on the basis of the quantity of electric charge stored in the  
capacitor element,  
the first circuit unit including a plurality of transistors connected in parallel,

respective gates of the first circuit unit transistors being mutually connected,  
and

the second circuit unit including a plurality of transistors connected in series,  
respective gates of the second circuit unit transistors being mutually  
connected,

the first circuit unit and the second circuit unit constituting a current mirror  
circuit.

5. (Currently Amended) An electronic circuit, comprising:

a first circuit unit through which a first current having a first current level  
passes;

a capacitor element to store a quantity of electric charge corresponding to the  
first current level; and

a second circuit unit to generate a second current having a second current level  
different from the first current level on the basis of the quantity of electric charge stored in the  
capacitor element;

at least one of the first circuit unit and the second circuit unit including a  
plurality of transistors connected in series or in parallel,

respective gates of the transistors being mutually connected, and

the electrical connections of the plurality of transistors being controlled by a  
control element,

the first circuit unit and the second circuit unit constituting a current mirror  
circuit.

6. (Previously Presented) The electronic circuit according to Claim 1, at least  
one of the plurality of transistors being a transistor common to the first circuit unit and the  
second circuit unit.

7. (Previously Presented) The electronic circuit according to Claim 1, the plurality of transistors having the same driving capability.
8. (Previously Presented) The electronic circuit according to Claim 1, the plurality of transistors being formed in a bundle.
9. (Original) The electronic circuit according to Claim 1, the first current level being higher than the second current level.
10. (Original) The electronic circuit according to Claim 1, the second current level being higher than the first current level.
11. (Original) The electronic circuit according to Claim 1, further comprising:  
electronic elements supplied with the second current.
12. (Original) The electronic circuit according to Claim 11, the electronic elements being electro-optical elements or current-driven elements.
13. (Original) The electronic circuit according to Claim 12, the electronic elements being organic EL elements.
14. (Currently Amended) An electronic device provided with a first signal line, a second signal line, and a plurality of unit circuits, each of the plurality of unit circuits comprising:  
  
a switching element connected to the first signal line, an on/off state of the switching element being controlled by switching signals supplied from the first signal line;  
  
a first circuit unit connected to the second signal line, a first current having a first current level supplied from the second signal line passing through the first circuit unit by switching on the switching element;  
  
a capacitor element to store a quantity of electric charge corresponding to the first current level; and

a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,

at least one of the first circuit unit and the second circuit unit including a plurality of transistors connected in series or in parallel,

respective gates of the transistors being mutually connected,

the first circuit unit and the second circuit unit constituting a current mirror circuit.

15. (Currently Amended) An electronic device provided with a first signal line, a second signal line, and a plurality of unit circuits, each of the plurality of unit circuits comprising:

a switching element connected to the first signal line, an on/off state of the switching element being controlled by switching signals supplied from the first signal line;

a first circuit unit connected to the second signal line, a first current having a first current level supplied from the second signal line passing through the first circuit unit by switching on the switching element;

a capacitor element to store a quantity of electric charge corresponding to the first current level; and

a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,

the first circuit unit including a plurality of transistors connected in parallel,

respective gates of the transistors being mutually connected,

the first circuit unit and the second circuit unit constituting a current mirror circuit.

16. (Currently Amended) An electronic device provided with a first signal line, a second signal line, and a plurality of unit circuits, each of the plurality of unit circuits comprising:

a switching element connected to the first signal line, an on/off state of the switching element being controlled by switching signals supplied from the first signal line;

a first circuit unit connected to the second signal line, a first current having a first current level supplied from the second signal line passing through the first circuit unit by switching on the switching element;

a capacitor element to store a quantity of electric charge corresponding to the first current level; and

a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,

the second circuit unit including a plurality of transistors connected in series, respective gates of the transistors being mutually connected,

the first circuit unit and the second circuit unit constituting a current mirror circuit.

17. (Currently Amended) An electronic device provided with a first signal line, a second signal line, and a plurality of unit circuits, each of the plurality of unit circuits comprising:

a switching element connected to the first signal line, an on/off state of the switching element being controlled by switching signals supplied from the first signal line;

a first circuit unit connected to the second signal line, a first current having a first current level supplied from the second signal line passing through the first circuit unit by switching on the switching element;

a capacitor element to store a quantity of electric charge corresponding to the first current level; and

a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,

the first circuit unit including a plurality of transistors connected in parallel, respective gates of the first circuit unit transistors being mutually connected, and

the second circuit unit including a plurality of unit elements connected in series,

respective gates of the second circuit unit transistors being mutually connected,

the first circuit unit and the second circuit unit constituting a current mirror circuit.

18. (Currently Amended) An electronic device provided with a first signal line, a second signal line, and a plurality of unit circuits, each of the plurality of unit circuits comprising:

a switching element connected to the first signal line, an on/off state of the switching element being controlled by switching signals supplied from the first signal line;

a first circuit unit connected to the second signal line, a first current having a first current level supplied from the second signal line passing through the first circuit unit by switching on the switching element;

a capacitor element to store a quantity of electric charge corresponding to the first current level; and

a second circuit unit to generate a second current having a second current level different from the first current level on the basis of the quantity of electric charge stored in the capacitor element,

at least one of the first circuit unit and the second circuit unit including a plurality of transistors connected in series or in parallel,

respective gates of the transistors being mutually connected, and

the electrical connections of the plurality of transistors being controlled by a control element,

the first circuit unit and the second circuit unit constituting a current mirror circuit.

19. (Previously Presented) The electronic device according to Claim 14, at least one of the plurality of transistors being a transistor common to the first circuit unit and the second circuit unit.

20. (Previously Presented) The electronic device according to Claim 14, the plurality of transistors having the same driving capability.

21. (Previously Presented) The electronic device according to Claim 14, the plurality of transistors being formed in a bundle.

22. (Original) The electronic device according to Claim 14, the first current level being higher than the second current level.

23. (Original) The electronic device according to Claim 14, the second current level being higher than the first current level.

24. (Original) The electronic device according to Claim 14, further comprising:  
electronic elements supplied with the second current.

25. (Original) The electronic device according to Claim 24, the electronic elements being electro-optical elements or current-driven elements.



26. (Original) The electronic device according to Claim 25, the electronic elements including organic EL elements.
27. (Original) An electronic apparatus having mounted therein the electronic circuit according to Claim 1.
28. (Original) An electronic apparatus having mounted therein the electronic device according to Claim 14.
29. (New) An electronic circuit, comprising:
- an organic El element;
  - a power source line;
  - a data line;
  - a storage capacitor;
  - a plurality of sub-scanning lines, comprising:
    - a first sub-scanning line;
    - a second sub-scanning line; and
    - a third sub-scanning line;
  - a plurality of switching transistors, comprising:
    - a first switching transistor;
    - a second switching transistor;
    - a third switching transistor;
    - a fourth switching transistor;
    - a fifth switching transistor;
    - a sixth switching transistor; and
    - a seventh switching transistor,
  - each of the plurality of switching transistors including a source electrode, a drain electrode and a gate electrode;

a plurality of driving transistors, comprising:

- a first driving transistor;
- a second driving transistor;
- a third driving transistor;
- a fourth driving transistor; and
- a fifth driving transistor,

each of the plurality of driving transistors including a source electrode, a drain electrode and a gate electrode;

the source electrode or the drain electrode of the first driving transistor being connected to the power source line, the source electrode or the drain electrode of the first driving transistor which is not connected to the power source line being connected to the source electrode or the drain electrode of the second driving transistor, and the source electrode or the drain electrode of the first driving transistor that is connected to the power source line also being connected to the source electrode or the drain electrode of the fourth switching transistor, and the source electrode or the drain electrode of the fourth switching transistor, which is not connected to the source electrode or the drain electrode of the first driving transistor, being connected to the source electrode or the drain electrode of the second driving transistor, which is not connected to the first driving transistor;

the source electrode or the drain electrode of the second driving transistor, which is connected to the fourth switching transistor, being connected to the drain electrode or the source electrode of the third driving transistor, the source electrode or the drain electrode of the second driving transistor, which is not connected to the drain electrode or the source electrode of the third driving transistor, being connected to the source electrode or the drain electrode of the sixth switching transistor, and the source electrode or the drain electrode of the sixth switching transistor, which is not connected to the source electrode or

the drain electrode of the second driving transistor, being connected to the source electrode or the drain electrode of the third driving transistor, which is not connected to the second driving transistor,

the electrode of the third driving transistor, which is connected to the source electrode or the drain electrode of the sixth switching transistor, being connected to the drain electrode or the source electrode of the fourth driving transistor, the source electrode or the drain electrode of the third driving transistor, which is not connected to the drain electrode or the source electrode of the fourth driving transistor, being connected to the source electrode or the drain electrode of the fifth switching transistor, the source electrode or the drain electrode of the fifth switching transistor, which is not connected to the source electrode or the drain electrode of the third driving transistor, being connected to the source electrode or the drain electrode of the fourth driving transistor, which is not connected to the third driving transistor,

the source electrode or the drain electrode of the fourth driving transistor, which is connected to the source electrode or the drain electrode of the fifth switching transistor, being connected to the source electrode or the drain electrode of the fifth driving transistor, the electrode of the fourth driving transistor, which is not connected to the drain electrode or the source electrode of the fifth switching transistor, being connected to the source electrode or the drain electrode of the seventh switching transistor, the electrode of the seventh switching transistor, which is not connected to the fourth driving transistor, being connected to the source electrode or the drain electrode of the fifth driving transistor which is not connected to the fourth driving transistor, the source electrode or the drain electrode of the fifth driving transistor being connected to the seventh switching transistor also being connected to the source electrode or the drain electrode of the first switching transistor,

the source electrode or the drain electrode of the first switching transistor which is not connected to the seventh switching transistor being connected to the data line that is connected to a data line driving circuit,

the respective gate electrodes of the plurality of driving transistors being connected to the storage capacitor and the source electrode or the drain electrode of the second switching transistor which is connected to the storage capacitor,

the electrode of the storage capacitor, which is not connected to the respective gate electrodes of the plurality of driving transistors, being connected to the power source line,

the source electrode or the drain electrode of the second switching transistor which is not connected to the storage capacitor being connected to the source electrode or the drain electrode of the first switching transistor which is connected to the seventh switching transistor and to the source electrode or the drain electrode of the third switching transistor,

the gate electrode of the second switching transistor and the gate electrode of the first switching transistor being connected to the first sub-scanning line,

the gate electrode of the third switching transistor being connected to the second sub-scanning line,

the source electrode and the drain electrode of the third switching transistor which is not connected to the first switching transistor being connected to an anode of the organic EL element, and

the cathode of the organic EL element being connected to a ground.